

Low-Power Deep Sub-Micron CMOS Logic: Sub-threshold Current Reduction (The Springer International Series in Engineering and Computer Science)



1. 1 Power-dissipation trends in CMOS circuits Shrinking device geometry, growing chip area and increased data-processing speed performance are technological trends in the integrated circuit industry to enlarge chip functionality. Already in 1965 Gordon Moore predicted that the total number of devices on a chip would double every year until the 1970s and every 24 months in the 1980s. This prediction is widely known as Moores Law and eventually culminated in the Semiconductor Industry Association (SIA) technology road map [1]. The SIA road map has been a guide for the industry leading them to continued wafer and die size growth, increased transistor density and operating frequencies, and defect density reduction. To mention a few numbers; the die size increased 7% per year, the smallest feature sizes decreased 30% and the operating frequencies doubled every two years. As a consequence of these trends both the number of transistors and the power dissipation per unit area increase. In the near future the maximum power dissipation per unit area will be reached. Down-scaling of the supply voltage is not only the most effective way to reduce power dissipation in general it also is a necessary precondition to ensure device reliability by reducing electrical fields and device temperature, to prevent device degradation. A draw-back of this solution is an increased signal propagation delay, which results in a lower data-processing speed performance.

[\[PDF\] Video, Stereo and Optoelectronics: Eighteen Advanced Electronics Projects](#)

[\[PDF\] American Dreamer](#)

[\[PDF\] The Origins of Development Economics: How Schools of Economic Thought Have Addressed Development](#)

[\[PDF\] explosive welding of metal sheets and Practice](#)

[\[PDF\] Subregionalism and World Order](#)

[\[PDF\] A Cup of Comfort for Couples: Stories that celebrate what it means to be in love \(Cup of Comfort \(Paperback\)\)](#)

[\[PDF\] Steve Jobs - My Take on Heaven - \(We of the Second Coming Book 1\)](#)

Low-Power Deep Sub-Micron CMOS Logic - Sub-threshold Current Low-Power Deep Sub-Micron CMOS Logic Kluwer International Series in Engineering and Computer Science pp 93-104. Weak-Inversion Current Reduction. **Weak-Inversion Current Reduction - Springer** The Springer International Series in Engineering and Computer Science Low-Power Deep Sub-Micron CMOS Logic. Sub-threshold Current Reduction. **Low-Power Deep Sub-Micron CMOS Logic: Sub-Threshold Current** Sub-threshold Current Reduction (The Springer International Series in International Series in Engineering and Computer Science) ePub **Low-Power Deep Sub-Micron CMOS Logic: Sub-threshold Current** Low-Power Deep Sub-Micron CMOS Logic: Sub-threshold Current The Springer International Series in Engineering and Computer Science. Low-Power Deep Sub-Micron CMOS Logic: Sub-threshold Current Reduction (The Springer International Series in Engineering and Computer Science) eBook: **Low-Power Deep Sub-Micron CMOS Logic - Sub-threshold Current** Read Low-Power Deep Sub-Micron CMOS Logic: Sub-threshold Current Reduction (The Springer International Series in Engineering and Computer Science) **Low-Power Deep Sub-Micron CMOS Logic: Sub-threshold Current** Low-Power Deep Sub-Micron CMOS Logic: Sub-threshold Current Reduction (The Springer International Series in Engineering and Computer Science) PDF Online. Speaking of books, more and more popping up online **Low-Power Deep Sub-Micron CMOS Logic: Sub-threshold Current** Low-Power Deep Sub-Micron CMOS Logic: Sub-threshold Current Reduction (The Springer International Series in Engineering and Computer Science) by Meer **Effectiveness of Weak-Inversion Current Reduction - Springer** Chapter. Low-Power Deep Sub-Micron CMOS Logic. Volume 841 of the series The Kluwer International Series in Engineering and Computer Science pp 11-52 **PDF Low-Power Deep Sub-Micron CMOS Logic: Sub-threshold** PDF Low-Power Deep Sub-Micron CMOS Logic: Sub-threshold Current Reduction (The Springer International Series in Engineering and Computer Science) **Low-Power Deep Sub-Micron CMOS Logic: Sub-threshold Current** Low-Power Deep Sub-Micron CMOS Logic. Sub-threshold Current Reduction. Series: The Springer International Series in Engineering and Computer Science, **Low-Power Deep Sub-Micron CMOS Logic: Sub-threshold Current** Low-Power Deep Sub-Micron CMOS Logic: Sub-Threshold Current THE SPRINGER INTERNATIONAL SERIES IN ENGINEERING AND COMPUTER **Power Dissipation in Digital CMOS Circuits - Springer** The Kluwer International Series in Engineering and Computer Science. Volume Low-Power Deep Sub-Micron CMOS Logic. Sub-threshold Current Reduction **Low-Power Deep Sub-Micron CMOS Logic: Sub-Threshold Current** 1 Power-dissipation trends in CMOS circuits Shrinking device geometry, growing chip area and increased data-processing speed The Springer International Series in Engineering and Computer Science Sub-threshold Current Reduction. **Low-Power Deep Sub-Micron CMOS Logic: Sub-threshold Current** Low-Power Deep Sub-Micron CMOS Logic: Sub-Threshold Current Reduction - The Springer International Series in Engineering and Computer Science 841 **Low-Power Deep Sub-Micron CMOS Logic: Sub-threshold Current** Low-Power Deep Sub-Micron CMOS Logic: Sub-threshold Current REDUCTION OF FUNCTIONAL POWER DISSIPATION. 53 Volume 841 of The Springer International Series in Engineering and Computer Science. **Low-Power Deep Sub-Micron CMOS Logic - Gunnarr Yishai - blogger** Low-Power Deep Sub-Micron CMOS Logic: Sub-threshold Current Reduction (The Springer International Series in Engineering and Computer Science). **Low-Power Deep Sub-Micron CMOS Logic - Springer** The Springer International Series in Engineering and Computer Science Low-Power Deep Sub-Micron CMOS Logic. Sub-threshold Current Reduction. **Low-Power Deep Sub-Micron CMOS Logic: Sub-threshold Current** : Low-Power Deep Sub-Micron CMOS Logic: Sub-threshold Current Reduction (The Springer International Series in Engineering and Computer Science): P. Therefore, a separate classification of sub-threshold current reduction **Low-Power Deep Sub-Micron CMOS Logic: Sub-threshold Current** **Low-Power Deep Sub-Micron CMOS Logic - Springer** Low-Power Deep Sub-Micron CMOS Logic: Sub-threshold Current Reduction. Front Cover **EFFECTIVENESS OF WEAKINVERSION CURRENT REDUCTION**. 105 The Springer International Series in Engineering and Computer Science. **Triple-S Circuit Designs - Springer** Low-Power Deep Sub-Micron CMOS Logic: Sub-threshold Current Reduction (The Springer International Series in Engineering and Computer Science) [P. van **PDF Low-Power Deep Sub-Micron CMOS Logic: Sub-threshold** Low-Power Deep Sub-Micron CMOS Logic. Volume 841 of the series The Kluwer International Series in Engineering and Computer Science pp 121-138 **Low-Power Deep Sub-Micron CMOS Logic: Sub-threshold Current** Low-Power Deep Sub-Micron CMOS Logic: Sub-Threshold Current Reduction (The Springer International Series in Engineering and Computer Science). **Low-Power Deep Sub-Micron CMOS Logic - Sub-threshold Current** The Springer International Series in Engineering and Computer Science Low-Power Deep Sub-Micron CMOS Logic. Sub-threshold Current Reduction. **Low-Power Deep Sub-Micron CMOS Logic: Sub-threshold Current** Low-Power Deep Sub-Micron CMOS Logic:

Sub-threshold Current Reduction (The Springer International Series in Engineering and Computer Science) - Kindle
9781475710571 - Low-power Deep Sub-micron Cmos Logic: Sub Low-Power Deep Sub-Micron CMOS Logic:
Sub-threshold Current Reduction (The Springer International Series in Engineering and Computer Science) PDF
Low-Power Deep Sub-Micron CMOS Logic - Sub-threshold Current Low-Power Deep Sub-Micron CMOS Logic.
Volume 841 of the series The Kluwer International Series in Engineering and Computer Science pp 105-120
Low-Power Deep Sub-Micron CMOS Logic: Sub-Threshold Current Low-Power Deep Sub-Micron CMOS Logic:
Sub-threshold Current Reduction (The Springer International Series in Engineering and Computer Science) eBook: