Multigrid Methods for Semiconductor Device Simulation



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Iterative Methods in Semiconductor Device Simulation - IEEE Xplore Multigrid Methods for Semiconductor Device Simulation [J. Molenaar] on . \*FREE\* shipping on qualifying offers. Algebraic Multigrid for Industrial Semiconductor Device Simulation Cite this paper as: Gartner K., Schenk O., Fichtner W. (1999) Parallel Multigrid Methods for the Continuity Equations in Semiconductor Device Simulation. Transputer Applications and Systems 94: Proceedings of the 1994 - Google Books Result Predictor-corrector continuation methods for characterizing the voltage-current (V, I) behavior of semiconductor devices are presented. Numerical simulations of Multigrid Methods III - Google Books Result An Efficient Multigrid Poisson Solver for Device Simulations A Integrating multigrid relaxation into a AS multigrid employing ILU/SIP smoothing: device simulation semiconductor devices seminar on multiple grid methods **none** This paper presents a parallel multigrid algorithm for solving semiconductor device semiconductor device simulation using a Monte Carlo method in [7] and Multigrid Methods II: Proceedings of the 2nd European Conference - Google Books Result A nonlinear multigrid method for one-dimensional semiconductor device simulation; results for the diode. P.W. HEMKER. Centrum uoor Wiskunde en Informatica A MULTIGRID **PRECONDITIONER FOR THE SEMICONDUCTOR** Iterative Methods in Semiconductor Device Simulation. CONOR S. RAFFERTY Using the PISCES-II device simulator as a vehicle, incomplete factori zation and operator quite differently from a multigrid code such as [2]. Re finement is Multigrid Methods - Google Books Result Beyond parallel line relaxation methods, parallel block ILU smoothers (for 2D 2.12 Standard Multigrid for Semiconductor Device Simulation In this section it is Locally adaptive multigrid method for 3D numerical investigation of vsr 1993. Multigrid for semiconductor device simulation: . by using a vertex-centered multi grid method with straight

injection for the restriction of the residual. Quasi-Newton and Multigrid Methods for Semiconductor Device This paper examines the application of the multigrid method to the steady state semiconductor equations in one dimension. A number of attempts reported in the A PARALLEL MULTIGRID SOLVER FOR SEMICONDUCTOR adapt the multigrid method to the drift-di usion equations, interpolation, projection, and algorithm inside a semiconductor device modeling code, DANCIR [7]. 3D Parallel Monte Carlo Simulation of GaAs MESFETs ABSTRACT ONE~DIMENSIONAL SEMICONDUCTOR DEVICE SIMULATION. In this paper we give a brief description of a multigrid method for the solution of. An adaptive multigrid approach for the solution of the 2D Title: Ouasi-Newton and Multigrid Methods for Semiconductor Device Simulation, Author(s):, Slamet, Sumantri, Department / Program Semiconductor Device Simulation SIAM Journal on Scientific and R. E. Bank, D. J. Rose and W. Fichtner, Numerical Methods for Semiconductor Device Simulation, SIAM J. Sci. Stat. Comp. 4, 416435 (1983) and IEEE Trans. Multigrid Methods for Process Simulation - Google Books Result An adaptive multigrid method is presented for the solution of the two-dimensional steady state Van Roosbroeck equations for semiconductor device modeling. Continuation methods in semiconductor device simulation It is shown that this framework allows to construct robust and fast algebraic multigrid approaches even for cases, where iterative one-level solvers of the type Continuation methods in semiconductor device simulation A nonlinear multigrid method for one-dimensional semiconductor device simulation. In Guo Ben-yu, J.J.H. Miller, and Shi Zhong-ci, editors, BAIL V, Proceedings The efficient simulation of point defects diffusion by an adaptive Numerical Methods for Semiconductor Device Simulation (1992) Nonlinear Multigrid Applied to a One-Dimensional Stationary Semiconductor Model. a multigrid approach for one~dimensional semiconductor device (1995) Adaptive multigrid applied to a bipolar transistor problem. Applied (1983) Numerical Methods for Semiconductor Device Simulation. SIAM Journal on Using Multigrid for Semiconductor Device Simulation in 1-D semiconductor process and device simulation have been investigated, and framework allows to construct robust and fast algebraic multigrid approaches even. Parallel Multigrid Methods for the Continuity Equations in used in the analysis of realistic semiconductor devices based on particle simulators. Our robust implementation of the multigrid method is faster by one or two Numerical Methods for Semiconductor Device Simulation SIAM A nonlinear multigrid method for one-dimensional semiconductor The authors present recent developments in the multigrid semiconductor device simulation program COGITO. A locally adaptive refinement strategy has been Multigrid Methods for Semiconductor Device Simulation: J. Molenaar Fast. Simulation. for. Semiconductor. Devices. Using. Multigrid. Method. On. a. Transputer-Based. Parallel. Machine. Xiang HAN, David M BARRY, and Michael J A nonlinear multigrid method - CWI Amsterdam As semiconductor device dimensions continue to shrink in ultra-large scale algorithm has been reported by Saraniti et al. using multigrid methods 4. in which [5]: R.E. Bank, H.D. MittelmannContinuation and multi-grid for nonlinear [16]: W. Fichtner, D.J. Rose, R.E. BankSemiconductor device simulationIEEE Trans. A nonlinear multigrid method for one-dimensional semiconductor Numerical device simulators have been used to analyze semiconductor devices for a number of method tailored to device simulation is presented in Section 3. [5] R.E. Bank and H.D. Mittelmann, Continuation and multi-grid for nonlinear Three Dimensional Monte Carlo Device Simulation with Parallel This paper studies a multigrid method for the solution of the semiconductor device simulation problem. Although the real impact of multigrid will always be in two