

An introduction to the fundamentals of semiconductor physics and engineering This book discusses fundamental semiconductor physics of devices and on-chip interconnections for physicists and links these concepts to engineering applications and case studies of computer chips. The book is organized in three parts. The first part deals with the representation of information and computation. The second part covers semiconductor device physics within the context of computation. The third part reviews chip design and semiconductor fabrication. The book includes relevant equations, with the aim of closing the gap in the existing literature with actual case studies and engineering applications. Examples are provided in each chapter to illustrate physical and electrical concepts through the use of high-performance silicon technologies. Introductory Semiconductor Device Physics for Chip Design and Manufacturing: Provides physical descriptions and illustrations with data visualizations to facilitate intuitive understanding of semiconductor physics, devices and on-chip interconnections Blends theoretical physics treatment with engineering applications and real case studies for manufactured chips Presents complementary-metal-oxide-semiconductor (CMOS) transistors in high-performance server microprocessors with static CMOS combinational digital circuit design examples Offers a rich array of student problem sets, mid-term exams, and final exams with a glossary at the end of the book M. Y. Lanzerotti, PhD, has over 15 years of engineering experience in designing integrated circuits for high-performance server chips and aerospace applications. Dr. Lanzerotti is Assistant Professor of Physics at Augsburg College and previously held positions as Associate Professor of Computer Engineering at Air Force Institute of Technology, Instructor at Harvard Summer School, Visiting Faculty Fellow at Pacific Lutheran University, Visiting Faculty Fellow at Sapienza University of Rome, and Research Staff Member at IBM Thomas J. Watson Research Center. This book is inspired from Dr. Lanzerotti's course, "Introductory Semiconductor Device Physics for Chip Design and Manufacturing," at Harvard Summer School. Dr. Lanzerotti holds physics degrees from Harvard College, the University of Cambridge, and Cornell University. Dr. Lanzerotti holds four U.S. patents, was awarded an IEEE Technical Innovation Award in 2007 and an IBM Outstanding Research Contribution Award in 1998, and was Editor-in-Chief of the IEEE Solid-State Circuits Society Magazine.

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