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VLSI Design In 2006 International Symposium on VLSI Design, Automation and Test, pp. 14, April 2006 21st International Conference on VLSI Design, 2008, pp. 261266 **Warin Sootkaneung - Google Scholar Citations**

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modeling of the NBTI effect for reliable design IEEE Transactions on Very Large Scale Integration (VLSI) Systems 18 (2), 173-183, VLSI Design, 2008. VLSID 2008. 21st International Conference on, 515-520, 2008. 16, 2008. **An**

Acceleration and Optimization Method for Optical - IEEE Xplore In: DAC 04: Proceedings of the 41st annual Design Automation Conference, pp. In: VLSI Design, 2008. VLSID 2008. 21st International Conference on, pp. 21.

VLSI Design 2008: Hyderabad, India. 21st International Conference on VLSI Design (VLSI Design 2008), 4-8 January

2008, Hyderabad, India. [contents] **A Timing-Driven Synthesis Technique for Arithmetic Product-of-Sum**
Published in: VLSI Design, 2008. VLSID 2008. 21st International Conference on. Article #:. Date of Conference: 4-8 Jan. 2008. Date Added to IEEE Xplore: 12 **Sudhakar Reddy - Google Scholar Citations** **On Common-Mode Skewed-Load and Broadside Tests - IEEE Xplore** Mathew, J., Rahaman, H., Jose, B.R., Pradhan, D.K.: Design of reversible finite field In: 21st International Conference on VLSI Design 2008, VLSID 2008, pp. **Ali A. Salehpour - Google Scholar Citations** NBTI degradation: A problem or a scare? In Proceedings of the VLSI Design, 2008: VLSID 2008 21st International Conference, (pp. 137 - 142). VLSI. Shakeri, K. **21st International Conference on VLSI Design (VLSID 2008)** 14th International Workshop, Leuven, Belgium, September 9-12, 2012, In: VLSID 2008: Proceedings of the 21st International Conference on VLSI Design, pp. **Progress in VLSI Design and Test: 16th International Symposium on - Google Books Result** Innovations in Information Technology, 2008. IIT 2008. VLSI Design, 2008. VLSID 2008. 21st International Conference on, 541-546, 2008. 9, 2008. **Sarvesh Bhardwaj - Google Scholar Citations** These improvements were verified on placed-and-routed designs as well. Published in: VLSI Design, 2008. VLSID 2008. 21st International Conference on. **Synthesis and Optimization of FPGA-Based Systems - Google Books Result** IEEE Transactions on Very Large Scale Integration (VLSI) Systems 17 (6), 770-780, 2009. 31, 2009 VLSID 2008. 21st International Conference on, 243-248, 2008. 4, 2008 Design methodologies for variation-aware integrated circuits. **Rupak Samanta - Google Scholar Citations** Published in: VLSI Design, 2008. VLSID 2008. 21st International Conference on. Article #:. Date of Conference: 4-8 Jan. 2008. Date Added to IEEE Xplore: 12 **DBLP: VLSI Design** Published in: VLSI Design, 2008. VLSID 2008. 21st International Conference on. Article #:. Date of Conference: 4-8 Jan. 2008. Date Added to IEEE Xplore: 12 **Implementing the Best Processor Cores - IEEE Xplore Document** TUT Press, Tallinn Baranov S (2008) Logic and system design of digital systems. In: Proceedings of the international symposium on VLSI design, automation and In: Proceedings of the 21st international conference on field-programmable **Research** Published in: VLSI Design, 2008. VLSID 2008. 21st International Conference on. Article #:. Date of Conference: 4-8 Jan. 2008. Date Added to IEEE Xplore: 12 **Power-efficient System Design - Google Books Result** Published in: VLSI Design, 2008. VLSID 2008. 21st International Conference on. Article #:. Date of Conference: 4-8 Jan. 2008. Date Added to IEEE Xplore: 12 **IEEE - 2008 21st International Conference on VLSI Design** Proceedings of the 2003 international symposium on Low power electronics and , 2003 Memory Technology, Design and Testing, 2007. VLSI Design, 2008. VLSID 2008. 21st International Conference on, 143-149, 2008. 26, 2008. **Cryptographic Hardware and Embedded Systems -- CHES 2012: 14th - Google Books Result** 16th International Symposium on VLSI Design and Test, VDAT 2012, Shipur, India, July of the 21st International Conference on VLSI Design, VLSID 2008, pp. **A Power Efficient Approach to Fault-Tolerant Register File Design** 21st International Conference on VLSI Design (VLSI Design 2008), 4-8 January 2008, Hyderabad, India. IEEE Computer Society 2008, ISBN 0-7695-3083-4 **Electronic Design Automation for IC System Design, Verification, - Google Books Result** In: DAC 84: Proceedings of the 21st conference on Design automation, pp. In: FPL 2003: 13th International Conference on Field Programmable Logic and In: DATE 2008: Proceedings of the Conference on Design, Automation and In: VLSID 07: Proceedings of the 20th International Conference on VLSI Design, pp. **Application Analysis Tools for ASIP Design: Application Profiling - Google Books Result** NBTI degradation: A problem or a scare? KK Saluja, S Vijayakumar, W Sootkaneung, X Yang. VLSI Design, 2008. VLSID 2008. 21st International Conference on **Field-Coupled Nanocomputing: Paradigms, Progress, and Perspectives - Google Books Result** 12th International Conference on VLSI Design (VLSI-99), pp 196-205, Goa, India, pp. .. 21st International Conference on VLSI Design 2008, January 4-8,